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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/597,353	07/06/2007	Yoshito Katano	09792909-6735	9812

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EXAMINER

WILSON, YOLANDA L

ART UNIT	PAPER NUMBER
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2113

MAIL DATE	DELIVERY MODE
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07/20/2009

PAPER

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Office Action Summary

Application No.

10/597,353

Applicant(s)

KATANO ET AL.

Examiner

Yolanda L. Wilson

Art Unit

2113

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --
Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 24 April 2009.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-8 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO/SI/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date _____
- 5) ☐ Notice of Informal Patent Application
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-6,8 are rejected under 35 U.S.C. 102(b) as being anticipated by Atkinson (US Publication Number 20020073358A1). As per claims 1 and 8, Atkinson discloses a semiconductor device and a data-rewritable nonvolatile memory, said data-rewritable nonvolatile memory having a plurality of data blocks wherein boot program instructions are stored in parallel said boot program instructions comprising a plurality of pages of data, each said page being stored in parallel in at least two data blocks, said semiconductor device comprising a central processing unit (CPU) and a read control circuit (RCC), wherein: the CPU is configured, in part, to specify to the RCC a read position for reading out each page of the boot program instructions stored in the data-rewritable nonvolatile memory at the starting time, said each page stored in parallel in at least a first respective data block and a second respective data block; and the RCC is configured to (a) determine whether the first respective data block is faulty or not according to data read out from the first respective block, (b) output the first data to the CPU if the block is determined as not faulty, and (c) read when the first respective data blocks is determined as faulty, second data from the second respective data block and

output said second data to the CPU when said second respective data block is determined as not faulty in paragraphs 0043-0045.

3. As per claim 2, Atkinson discloses wherein the read control circuit is configured to determine whether the block is faulty or not faulty at least according to an error correction code contained in the data read out from the data-rewritable nonvolatile memory in paragraphs 0044-0045.

4. As per claim 3, Atkinson discloses wherein the read control circuit corrects the data and supplies it to the CPU when it determines that the data is correctable according to the error correction code but otherwise determines that the block is faulty when it determines that the data is uncorrectable data in paragraphs 0044-0045.

5. As per claim 4, Atkinson discloses wherein the read control circuit is configured to determine that the block is faulty or not faulty at least according to a block state information contained in the data read out from the data-rewritable nonvolatile memory in paragraphs 0044-0045.

6. As per claim 5, Atkinson discloses wherein the read control circuit determines that the block is faulty when the block state information does not show a predetermined value in paragraphs 0044-0045.

7. As per claim 6, Atkinson discloses wherein the block state information is stored in a leading page of each of the blocks storing boot program instructions in paragraphs 0044-0045.

Claim Rejections - 35 USC § 103

8. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

9. Claim 7 is rejected under 35 U.S.C. 103(a) as being unpatentable over Atkinson in view of Aasheim et al. (USPN 7178061B2). As per claim 7, Atkinson and Hashimoto fail to explicitly state wherein the data-rewritable nonvolatile memory is a NAND type flash memory.

Aasheim et al. discloses this limitation in column 9, lines 20-25.

Accordingly, a person of ordinary skill in the art would be motivated to have the data-rewritable nonvolatile memory is a NAND type flash memory. A person of ordinary skill in the art would be motivated to have the data-rewritable nonvolatile memory is a NAND type flash memory because NAND type flash memory is a known type of non-volatile memory used for storing boot programs.

Claim Rejections - 35 USC § 112

10. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

11. Claims 1,8 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one

skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. The newly added limitations 'said boot program instructions comprising a plurality of pages of data, each said page being stored in parallel in at least two data blocks' and 'said each page being stored in parallel in at least a first respective data block and a second respective data block' are not disclosed in the specification. Please correct accordingly.

Response to Arguments

12. Applicant's arguments with respect to claims 1-8 have been considered but are moot in view of the new ground(s) of rejection. The newly added limitations to the claims required a new reference to be found. Please see the above rejection.

Conclusion

13. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of

the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Yolanda L. Wilson whose telephone number is (571) 272-3653. The examiner can normally be reached on M-F (7:30-4:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Beausoliel can be reached on (571) 272-3645. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

/Yolanda L Wilson/
Primary Examiner, Art Unit 2113